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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/780,603	02/19/2004	Osamu Aizawa	1614.1384	3177

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EXAMINER

DINH, TUAN T

ART UNIT PAPER NUMBER

2841

DATE MAILED: 09/16/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/780,603

Applicant(s)

AIZAWA, OSAMU

Examiner

Tuan T. Dinh

Art Unit

2841

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 July 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) 5 and 6 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 2, 4 and 7 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 19 February 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>03/25/05</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Applicant's election without traverse of Specie I (figures 4-8, claims 1-4, and 7) in the reply filed on 07/05/05 is acknowledged.

Drawings

2. **Figures 1-3** should be designated by a legend such as **--Prior Art--** because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.
3. The drawings are objected to under 37 CFR 1.83(a) because they fail to show "an electronic parts mounting regions 77-1, 78-3 as shown in specie I (figures 4-8)", and "the daughter board...the lower end thereof, claim 3, lines 7-10" as described in the specification. Any structural detail that is essential for a proper understanding of the disclosed invention should be shown in the drawing. MPEP § 608.02(d). Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an

amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1-2, 4, and 7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Potter et al. (U.S. Patent 6,533,587) in view of Hsu (U.S. Patent 6,388,895).

As to claims 1, 7, Potter et al. discloses a printed board unit of an electronic apparatus as shown in figure 1 comprising:

a mother board (20, column 3, lines 18-19); and

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first and second board units (12, 14) that are vertically mounted onto the mother board (20) and face each other,

the first board unit including a vertically standing daughter board (12), and an electronic parts mounting board (14) that horizontally protrudes from the daughter board,

the second board unit including vertically standing daughter board (12), and an electronic parts mounting board (14) that horizontally protrudes from the daughter board,

the electronic parts mounting boards (14) of the first and second board units facing each other.

Potter does not disclose the mounting boards of each unit that being located at different heights, and overlapping with each other when seen from the tops of the daughter boards.

Hsu teaches a telecommunication main distribution frame structure as shown in figures 1-2 comprising first and second boards (10, 20) having busses (70) plug into sockets (60) on the boards, the busses (70) having different height connected to the sockets (60), and overlapping with each other.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have teaching of Hsu applied on the PCB unit of Potter in order to provide an easy installation, save space, and easy to trouble shoot if one of the cards to be fails on test.

As to claim 2, Potter et al. discloses a printed board unit as shown in figure 1 comprising:

a mother board (20); and

first board unit and second board units (12, 14) that are vertically mounted onto the mother board (20) and face each other,

the first board unit including a vertically standing daughter board (12) having a connector (13), and an electronic parts mounting board (14) connected to the daughter board (12) and horizontally protrudes from the daughter board (12),

the second board unit including vertically standing daughter board (12) having a connector (10), and an electronic parts mounting board (14) connected the daughter board (12) and horizontally protrudes from the daughter board (12),

the electronic parts mounting board of first board unit being located on the lower side while the electronic parts mounting board of the second board unit is located on the upper side, the electronic parts mounting boards (14) of the first and second board units facing each other.

Potter does not discloses the connector (13, 10) of the first and second board units being formed on lower and upper regions of each of the daughter boards (12), and overlapping with each other when seen from the tops of the daughter boards.

Hsu teaches a telecommunication main distribution frame structure as shown in figures 1-2 comprising first and second boards (10, 20) having busses (70) plug into sockets (60) on the boards, the busses (70) having different height connected to the sockets (60), and overlapping with each other.

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It would have been obvious to one having ordinary skill in the art at the time the invention was made to have teaching of Hsu applied on the PCB unit of Potter in order to provide an easy installation, save space, and easy to trouble shoot if one of the cards to be fails on test.

As to claim 4, Potter et al. discloses a semiconductor part (components formed on the motherboard 20) mounted between the daughter boards of the first and second board units on mother board (20); and the semiconductor part is accommodated a space that is formed below the electronic parts mounting board of the first board unit, see figure 1.

Allowable Subject Matter

6. Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Johnson et al. and Vinson et al. disclose related art.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Dinh whose telephone number is 571-272-1929. The examiner can normally be reached on M-F.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Tuan Dinh', with a long, sweeping horizontal stroke extending to the right.

Tuan Dinh
September 12, 2005.